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CONFIRMATION NO. 6596

SERIAL NUMBER 10/618,514	FILING DATE 07/11/2003 RULE	CLASS 438		GROUP ART UNIT 2823		ATTORNEY DOCKET NO. AMD-H0552			
APPLICANTS									
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** FOREIGN APPLICATIONS ************************************									
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 10/09/2003									
Foreign Priority claimed			STATE OR	SHEETS	TOTAL		INDEPENDENT		
Verified and Acknowledged Examiner's Signature Initials			COUNTRY CA	DRAWING 6	CLAIMS 21		CLAIMS 3		
ADDRESS WAGNER, MURABITO & HAO LLP Third Floor Two North Market Street San Jose , CA 95113									
TITLE Method of fabricating semiconductor device having triple LDD structure and lower gate resistance formed with a single implant process									
					☐ All Fees				
FILING FEE FEE	EES: Authority has been given in Paper			<u> </u>	☐ 1.16 Fees (Filing) ☐ 1.17 Fees (Processing Ext. of time)				
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